

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	5	vmm with interpreter	US-PGPUB; USPAT	OR	OFF	2008/01/01 16:49
L2	3	@ad<"20030627" and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/01 16:52
L3	3	2 and (translation translat\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/01 16:51
L4	0	3 and vliw	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/01 16:51
L5	7	interpreter and ((translation translat\$4) with vliw) and (vm "virtual machine")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/01 16:52
L6	6	@ad<"20030627" and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/01 17:02
L7	5	6 and exception	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/01 17:06
L8	3	7 and monitor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/01 17:05

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L9	1	7 and vmm	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/01 17:05
L10	1	6 and vmm	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/01 17:06
L11	5	6 and manager	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/01 18:39
L12	3	11 and "86"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/01 18:39
L13	3	11 and x86	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/01 18:39
S3	0	"10609158".ap.	US-PGPUB; USPAT	OR	OFF	2008/01/01 16:49
S45	0	("vliwwithtranslat\$4").PN.	US-PGPUB; USPAT	OR	OFF	2007/12/30 11:57
S46	122	vliw with translat\$4	US-PGPUB; USPAT	OR	OFF	2007/12/30 11:57
S47	96	vliw with translat\$4 with instruction\$1	US-PGPUB; USPAT	OR	OFF	2007/12/30 11:57
S48	24475917	@ad<"20030627"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/30 11:58
S49	64	S47 and S48	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/30 11:58

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S50	0	S49 and 718/1.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/30 11:58
S51	1	("6065078").PN.	USPAT	OR	OFF	2007/12/31 15:01
S52	1	S51 and port	USPAT	OR	ON	2007/12/31 15:01



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B Alpern, CR Attanasio, JJ Barton, MG Burke, P ... - IBM Systems Journal, 2000 - research.ibm.com

... compiler will play a role similar to HotSpot's **interpreter**. ... It uses a **VLIW** tree-like representation for ... 22] Conclusions Jalapeno is a **virtual machine** for Java ...

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[Pipelined Java Virtual Machine Interpreters - all 6 versions »](#)

J Hoogerbrugge, L Augusteijn - Compiler Construction: 9th International Conference, CC 2000 ... , 2000 - books.google.com

... not a severe problem because compiler and **interpreter** technology are ... On the TriMedia **VLIW** processor, with load and jump ... The Java **Virtual Machine** Specification. ...

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[A code compression system based on pipelined interpreters - all 7 versions »](#)

J Hoogerbrugge, L Augusteijn, J Trum, R van de ... - Software Practice and Experience, 1999 - doi.wiley.com

... speed of the pipelined **interpreter**; these were ... compaction; code size; interpreters; pipelining; **VLIW**, **ILP** ... a WWW browser, a Java **virtual machine**, an electronic ...

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[Hardware Realization of a Java Virtual Machine for High Performance Multimedia Applications - all 7 versions »](#)

M Berekovic, H Kloos, P Pirsch - The Journal of VLSI Signal Processing, 1999 - Springer

... of a JVM with a software **interpreter**. ... Java **Virtual Machine** for High Performance Multimedia Applications ... or more bytecodes into multiple (**VLIW**) RISC-instruction ...

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[\[PDF\] A high speed Prolog implementation on a VLIW processor. - all 5 versions »](#)

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... payed by the expense of implementing the **virtual machine**. ... language Prolog on the **VLIW** processor SAMP ... feeding a simulator with the microcoded **interpreter** and the W ...

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[\[PDF\] An Overview of Virtual Machine Architectures - all 8 versions »](#)

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... VM **Interpreter/Compiler** ... and early 1970s and were the origin of the term "**Virtual Machine**". ... underlying hardware happens to use a native **VLIW** instruction set ...

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... The interpretation of a **virtual machine** instruction consists of ... for the Phillips Trimdia **VLIW** processor, which has ... them to greatly speed up their **interpreter**. ...

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... 5], caching a few stack operands in registers, is a common technique to improve the performance of an **interpreter** for a stack-based **virtual machine** language. ...

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C Cifuentes, B Lewis, D Ung - Workshop on Binary Translation, 2002 - research.sun.com

... Set from Yorktown), is a **VLW virtual machine** that emulates ... determining what features a new **VLW** architecture should ... Aries combines a fast **interpreter** with a ...

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... An **interpreter** for a stack-based **virtual machine** language such as ... The WT+PHC **interpreter**

needs to reserve two more ... registers, such as the Intel **x86** processors. ...

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... microprocessor might be a DSP, a **VLIW**, or a ... within a separate address space, and within a **virtual machine**. ... can be a simple command **interpreter** that configures ...

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